

Application No. 10730167 (Docket: CNTR.2224-C1)  
37 CFR 1.111 Amendment dated 03/10/2008  
Reply to Office Action of 12/13/2007

### **AMENDMENTS TO THE ABSTRACT**

Please delete the section entitled "ABSTRACT OF THE DISCLOSURE" in its entirety and substitute the following section therefor:

#### **ABSTRACT OF THE DISCLOSURE**

A microprocessor apparatus is provided, for performing a cryptographic operation. The microprocessor apparatus includes fetch logic, a cryptography unit, and an integer unit. The fetch logic ~~that is configured~~ is configured to fetch an instruction flow from memory for execution by a microprocessor. The instruction flow includes an instruction that directs the microprocessor to perform the cryptographic operation. The instruction has an opcode field and a repeat prefix field. The opcode field prescribes that the device accomplish the cryptographic operation as further specified within a control word stored in a memory. The repeat prefix field is coupled to the opcode field. The repeat prefix field indicates that the cryptographic operation prescribed by the instruction is to be accomplished on a plurality of blocks of input data.